

APPENDIX A: PARALLEL I/O CONNECTIONS

Input connector pin no.	Remote I/O module	Port Address	Register bit
47	0	Fx00	0
45	1	-	1
43	2	-	2
41	3	-	3
39	4	-	4
37	5	-	5
35	6	-	6
33	7	-	7
31	8	Fx01	0
29	9	-	1
27	10	-	2
25	11	-	3
23	12	-	4
21	13	-	5
19	14	-	6
17	15	-	7
15	16	Fx02	0
13	17	-	1
11	18	-	2
9	19	-	3
7	20	-	4
5	21	-	5
3	22	-	6
1	23	-	7

All even numbered pins on both *PARALLEL* connectors are connected to ground. Pin 49 carries the Model 52's +5 VDC supply.

Note: Fx00 refers either to address F600 for output or address F800 for input. Output port F600 may be programmed for input